



ASA-1026

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

K. WATANABE et al.

Serial No. 09/939,589

Group Art Unit: 2133

Filed: August 28, 2001

Examiner: D. Gandhi

For: SEMICONDUCTOR INTEGRATED CIRCUIT
HAVING A SELF-TESTING FUNCTION


REQUEST FOR APPROVAL OF PROPOSED DRAWING CORRECTIONS

Commissioner for Patents
Mail Stop PGPUB Drawings
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The Applicants request approval of the proposed corrected
Figures 1, 4, 5 and 6 attached hereto.

Respectfully submitted,


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Date: September 2, 2004